

Osnovni RS flip-flop

Characteristic table			
S	R	Q_{next}	Action
0	0	Q	hold state
0	1	0	reset
1	0	1	set
1	1	X	not allowed



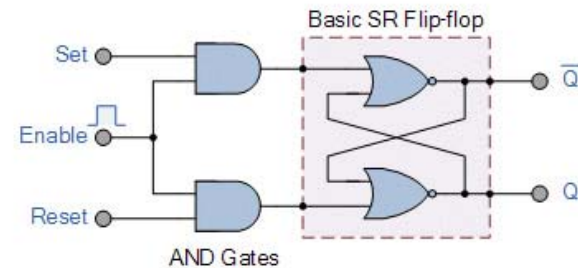
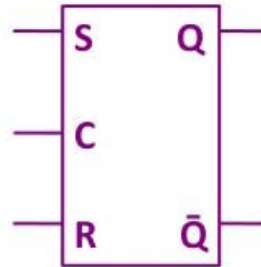
Sinhroni RS flip-flop

A clocked *SR* flip-flop.

R	S	$Enable$	Q_n
0	0	×	Q_{n-1}
0	1	1	1
1	0	1	0
1	1	1	Not allowed
×	×	0	Q_{n-1}

Truth table

Logical Symbol

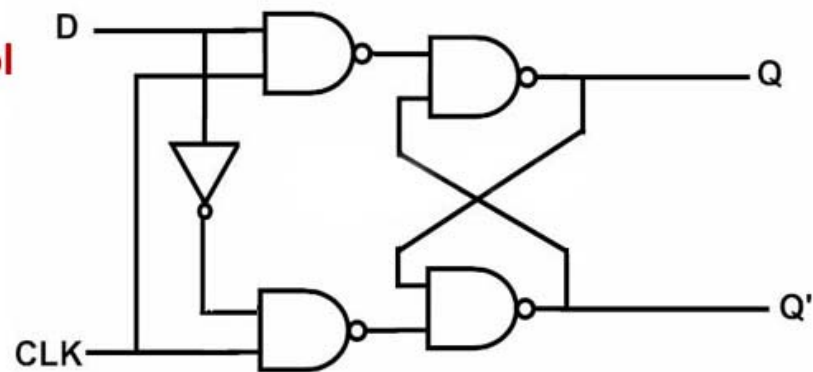
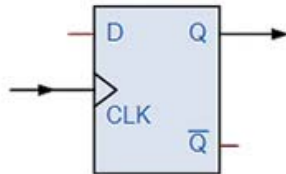


Sinhroni D flip-flop napravljen od RS

D	C	Q	$\bar{Q}$
x	0	no change	
0	p	0	1
1	p	1	0

Truth table

Logical Symbol



Paralelni registri sa D flip-flopom

