



PROF.DR ALEKSANDAR PEULIC

# PROJEKTOVANJE VLSI

ALEKSANDAR PEULIC



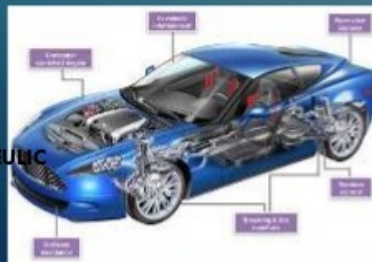
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# TREND RAZVOJA INDUSTRIJE POLUPROVODNIKA

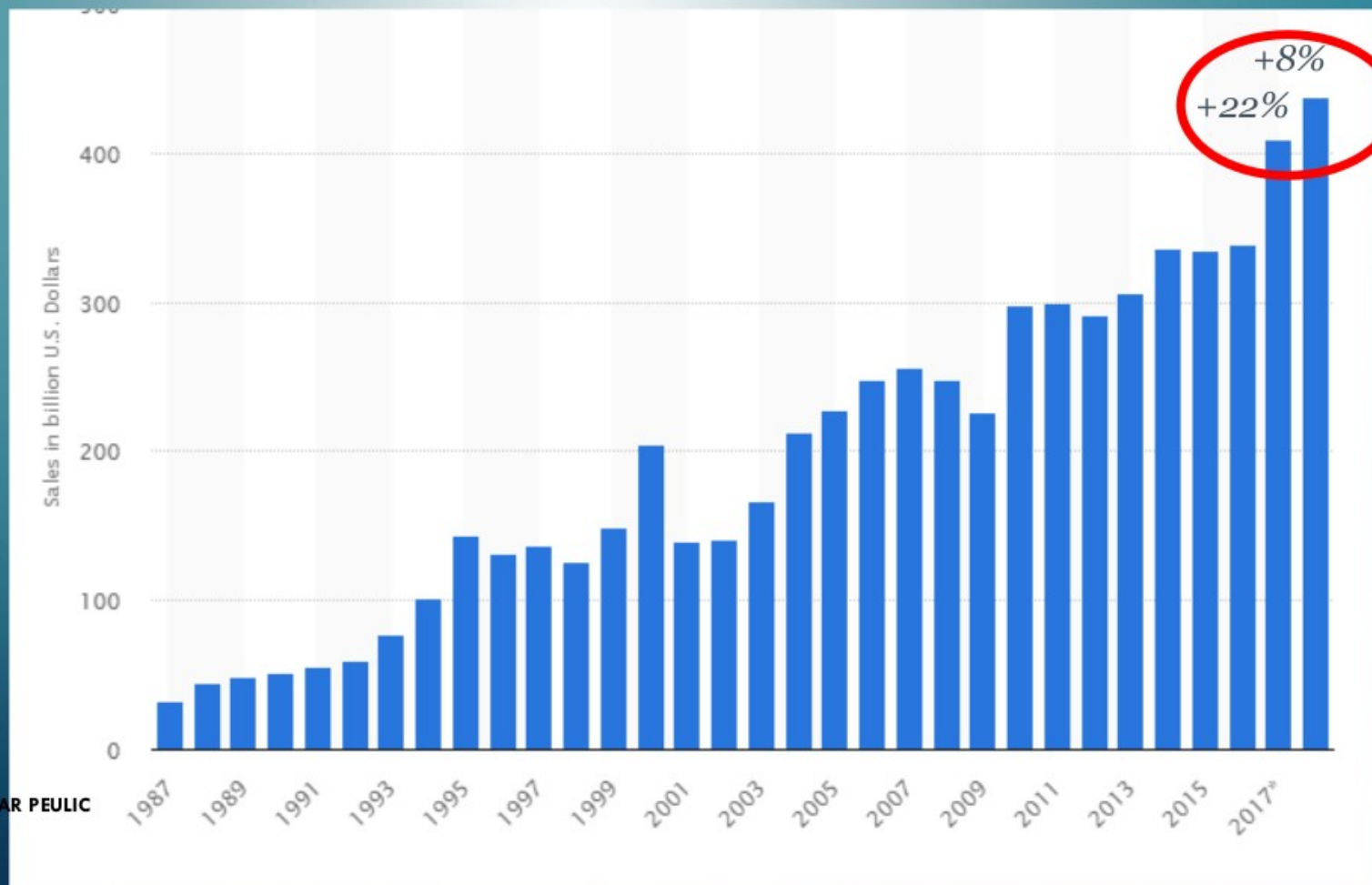
# GDE SE NALAZE ČIPOVI ?



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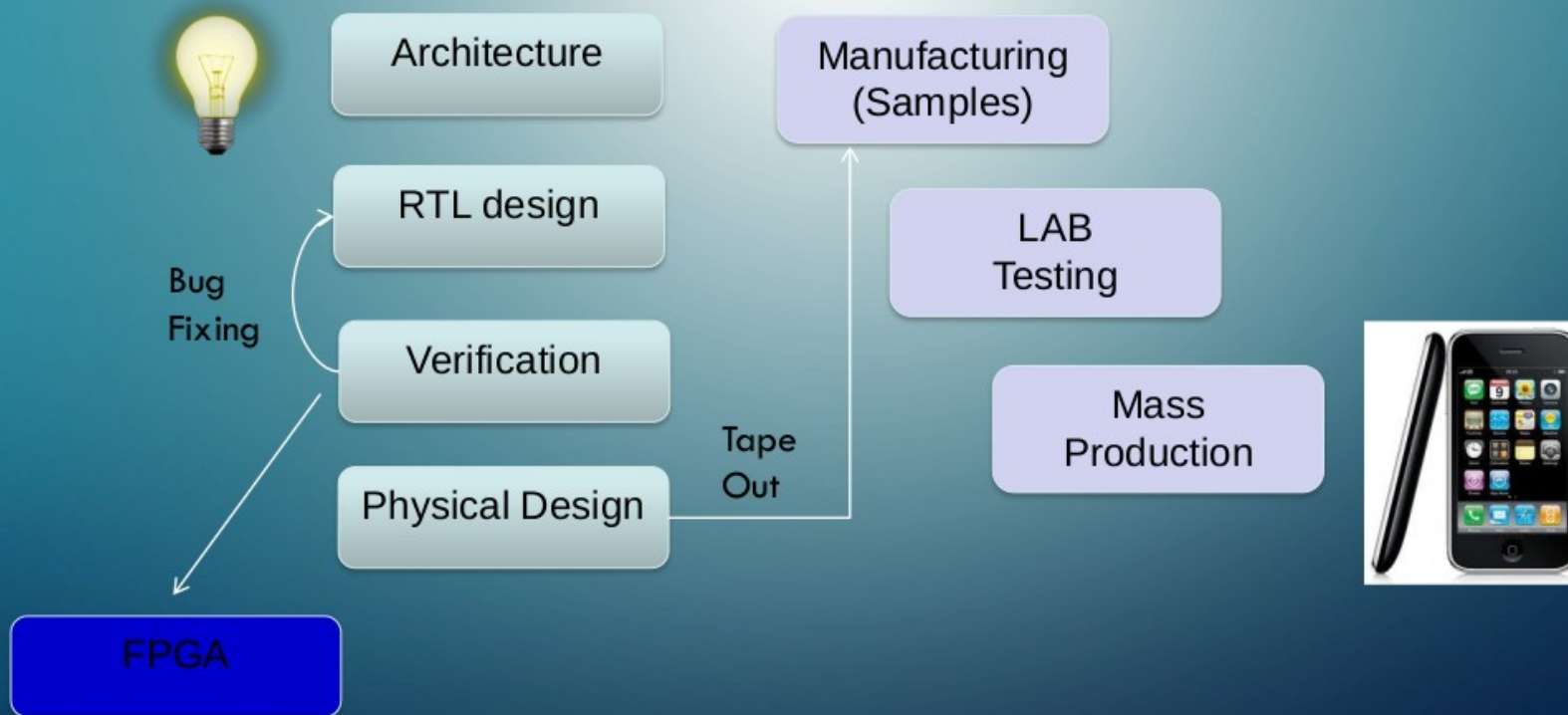


# PRODAJA ČIPOVA U BILIONIMA USA DOLARA!



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## Tok razvoja jednog čipa – Glavni koraci





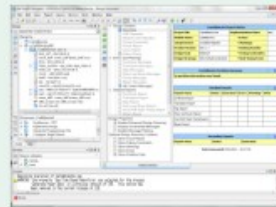
```

for (aa = 0 ; aa < 'd 13' ; aa++)
    [index out of range]
    if ((rx data align[aa] == K285M) || (rx data align[aa] == K285P))
        chan aligned <= 1'b1;
    this doesn't happen
  
```

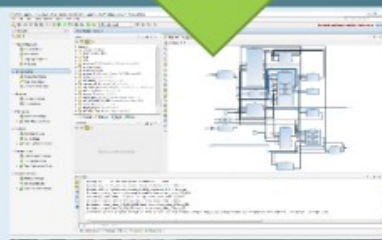


# XILINX VIVADO DESIGN SUITE

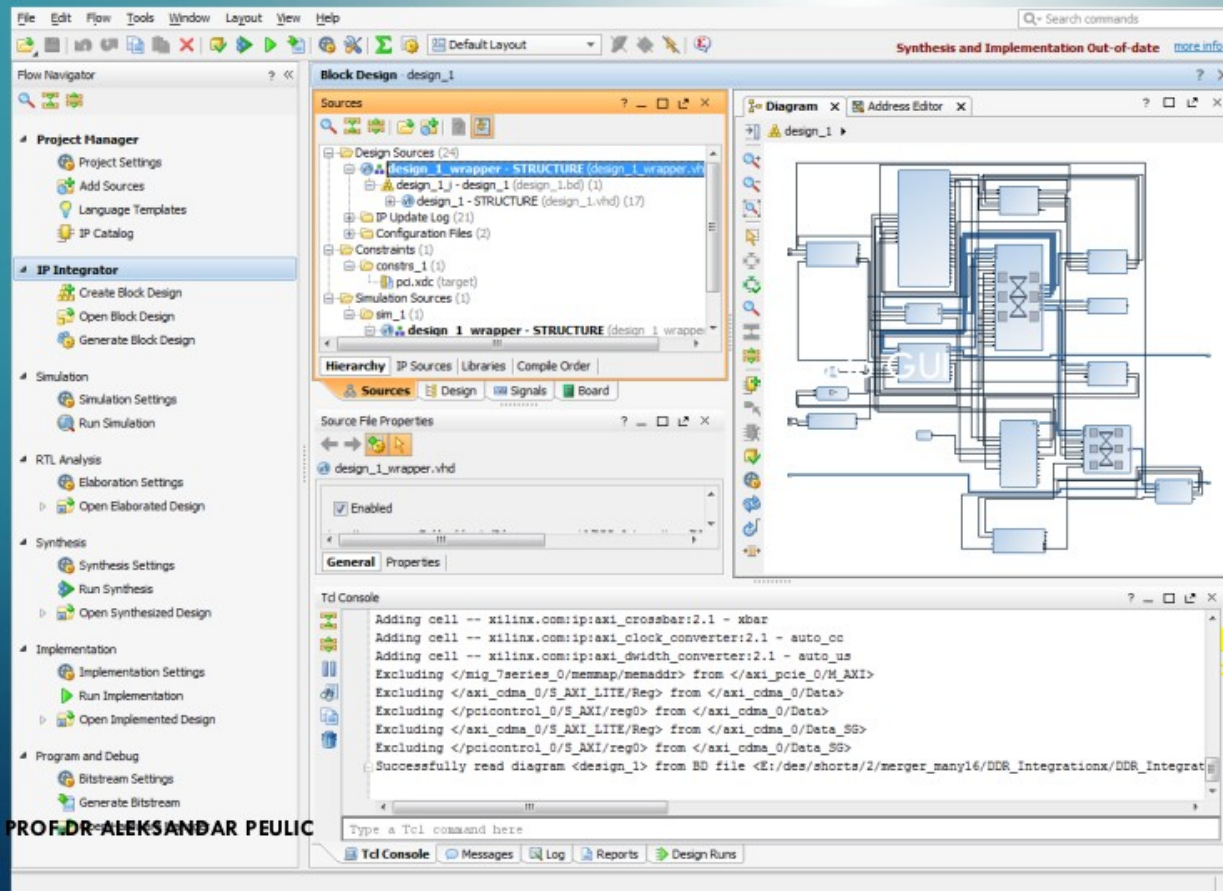
ISE



Vivado



# VIVADO GUI



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HARDWARE:  
RAZVOJNE PLOCE!!!!



# ORGANIZACIJA PREDMETA

- PREDAVANJA
  - VEZBE
  - PROJEKAT – INDIVIDUALNO
- } KOLOKVIJUM

# Razvojna ploca za vezbe PYNQ-Z2

