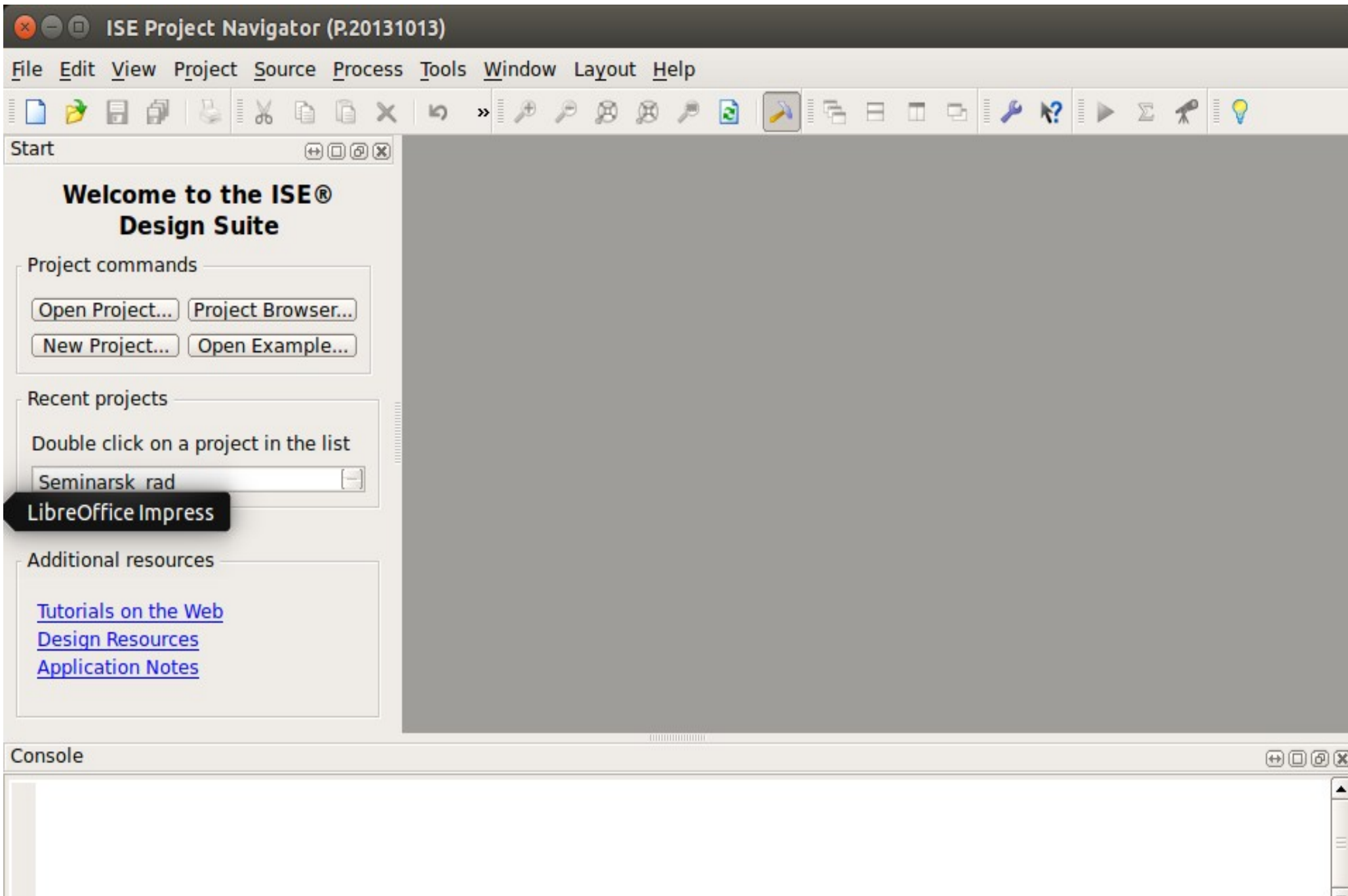




ISE Project Navigator (P.20131013) - /home/aleksandar/Projekti/Xilinx_Primeri/dekoder_3_8/Seminarsk_rad/Seminarsk_rad.xi

File Edit View Project Source Process Add Tools Window Layout Help

Options

Select Options

When you click on a branch:

- ☒ Select the entire branch
- ☐ Select the line segment

When you move an object:

- ☒ Keep the connections to other objects
- ☐ Break the connections to other objects

When you use the area select tool, select the

Net = A2
Port polarity = Input
Branch count = 1
I/O Marker count = 1

Design Summary Main.sch

Console

Launching Design Summary/Report Viewer...

Started : "Launching Schematic Editor to edit Main.sch".

Symbol Wizard

Source Page

Select the source for pin names and the symbol shape.

Pin name source

☐ Specify manually

☒ Using schematic

Main

☐ Using symbol

☐ Import symbol attributes

Shape

☒ Do not use reference symbol

☒ Rectangle

☐ Square

☐ Use reference symbol

Browse...

More Info

Next >

Cancel

Symbol Wizard

Pin Page

Define the pins to be placed on the symbol shape.

Symbol name:

Log_kolo_1

Pin definitions:

	Name ▾	Polarity	Side	Order
1	A0	Input ▾	Left ▾	3 ▾
2	A1	Input ▾	Left ▾	2 ▾
3	A2	Input ▾	Left ▾	1 ▾
4	D0	Output ▾	Right ▾	1 ▾
5	D1	Output ▾	Right ▾	2 ▾
6	D2	Output ▾	Right ▾	3 ▾

Add Pin

Remove Pin/Spacer

Insert Spacer

Move Spacer Up

Move Spacer Down

Keyboard Usage Tips:

- Use Tab and Shift-Tab to go to previous/next cell inside grid
- Use Tab and Shift-Tab to change focus among controls

More Info

< Back

Next >

Cancel

Symbol Wizard

Layout Page

Define the layout for various elements of the symbol.

Symbol name font size:

56

Pin name font size:

24

Pin length:

64

Pin space:

64

Pin edge:

32

Symbol width:

256

Symbol origin:

Left Bottom

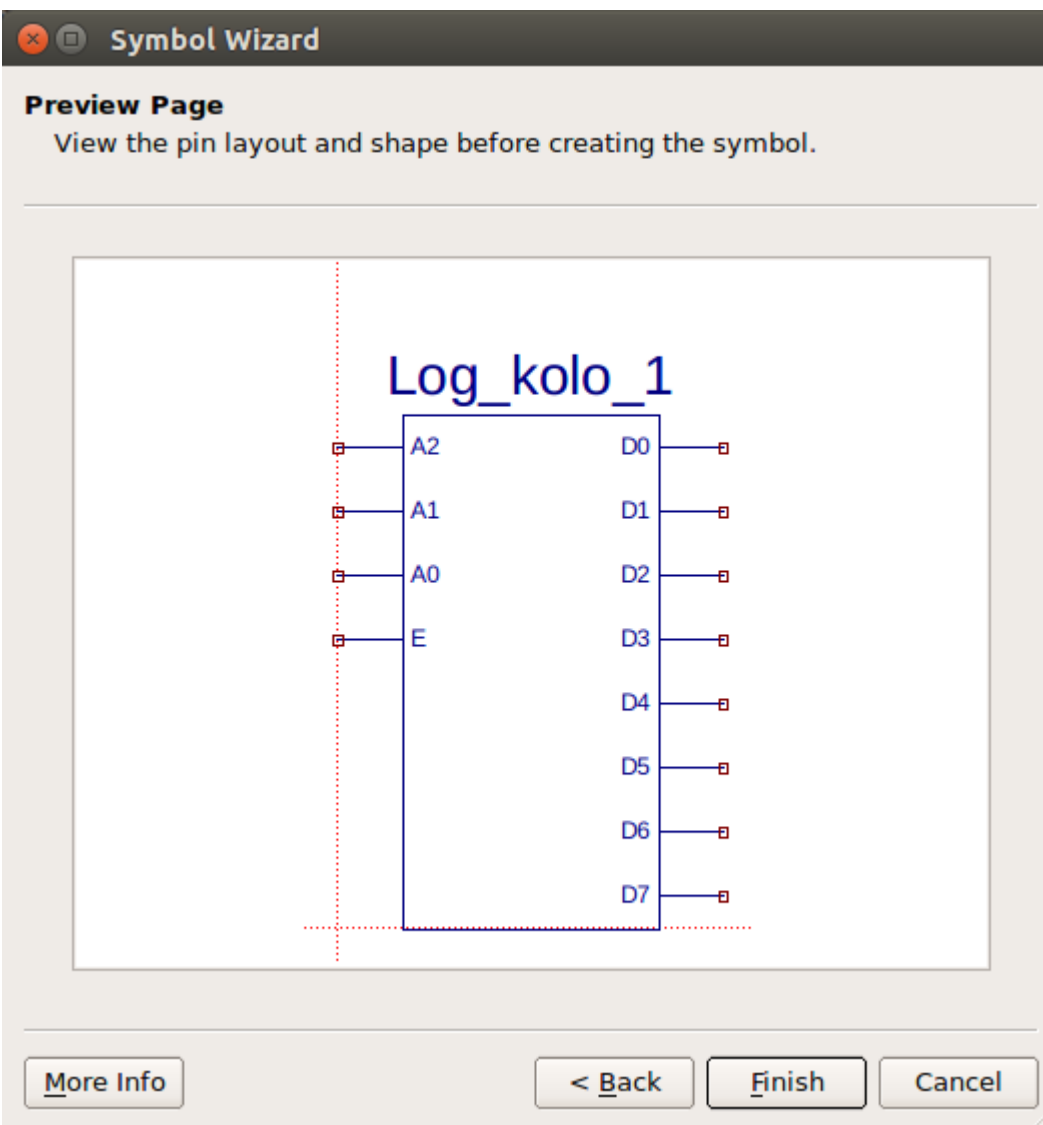
The diagram shows a rectangular symbol for a flip-flop. At the top center is the text 'FD'. Inside the rectangle, on the left, is a 'D' input and on the right is a 'Q' output. At the bottom center is a clock input 'C' with a triangle pointing into the rectangle. A dashed red line at the bottom-left corner indicates the 'symbol origin'. Green arrows and labels indicate various dimensions: 'symbol name font size' points to 'FD'; 'pin name font size' points to 'D' and 'Q'; 'pin length' points to the vertical distance from the top of the rectangle to the 'D' and 'Q' pins; 'pin space' points to the vertical distance between the 'D' and 'C' pins; 'pin edge' points to the vertical distance from the 'D' pin to the top edge; and 'symbol width' points to the horizontal width of the rectangle.

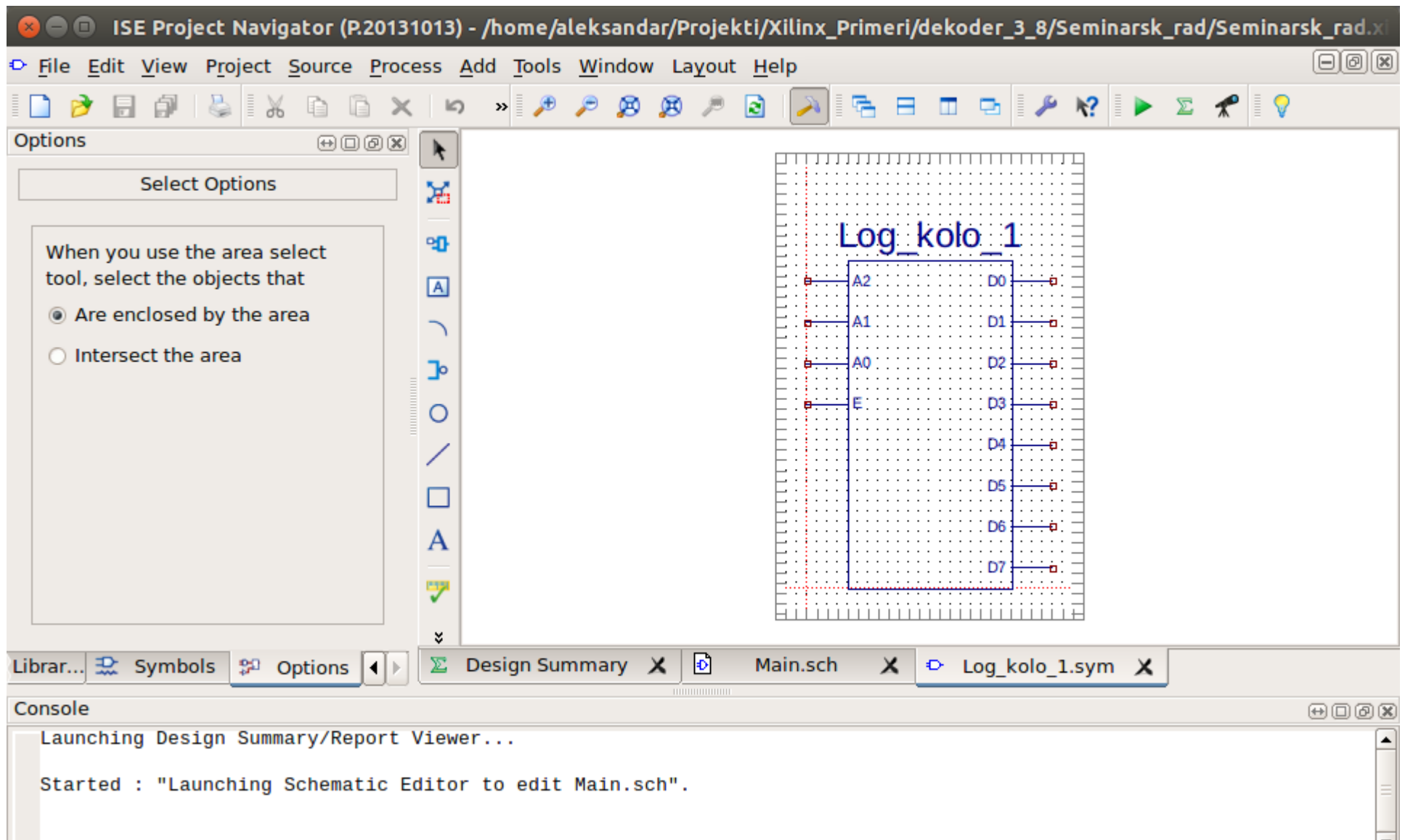
More Info

< Back

Next >

Cancel





New Source Wizard

Select Source Type

Select source type, file name and its location.

 BMM File

 ChipScope Definition and Connection File

 Implementation Constraints File

 IP (CORE Generator & Architecture Wizard)

 MEM File

 Schematic

 User Document

 Verilog Module

 Verilog Test Fixture

 VHDL Module

 VHDL Library

 VHDL Package

 VHDL Test Bench

 Embedded Processor

File name:

Nova_sema

Location:

x_Primeri/dekoder_3_8/Seminarsk_rad

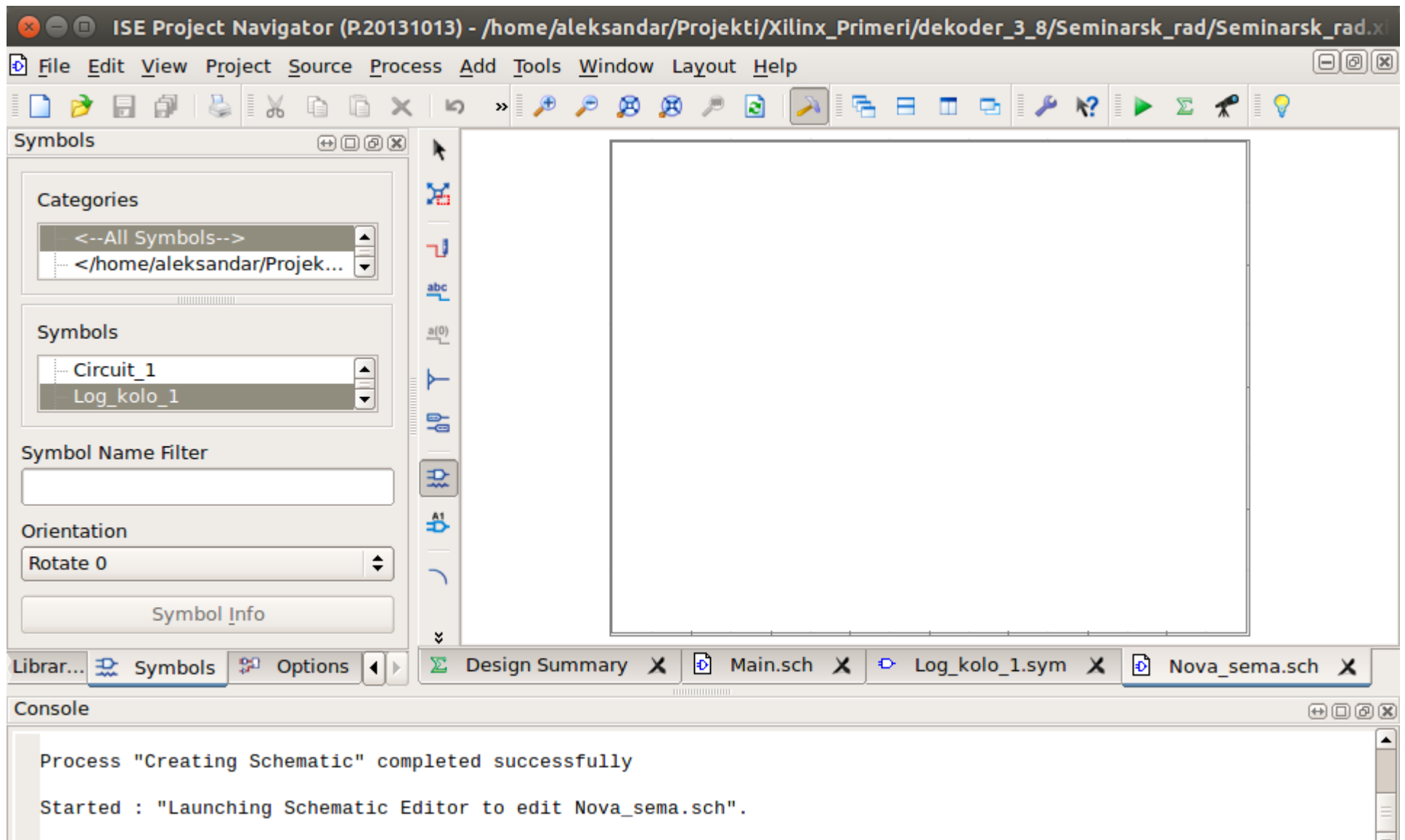
...

☒ Add to project

More Info

Next >

Cancel



Xilinx IDE

ISE Project Navigator (P.20131013) - /home/aleksandar/Projekti/Xilinx_Primeri/dekoder_3_8/Seminarsk_rad/Seminarsk_rad.x

File Edit View Project Source Process Add Tools Window Layout Help

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Log_kolo_1

A2 A1 A0 E

D0 D1 D2 D3 D4 D5 D6 D7

Design Summary Main.sch Log_kolo_1.sym Nova_sema.sch*

Console

Process "Creating Schematic" completed successfully

Started : "Launching Schematic Editor to edit Nova_sema.sch".

