



PROF.DR ALEKSANDAR PEULIC

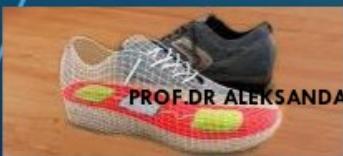
PROJEKTOVANJE VLSI

ALEKSANDAR PEULIC

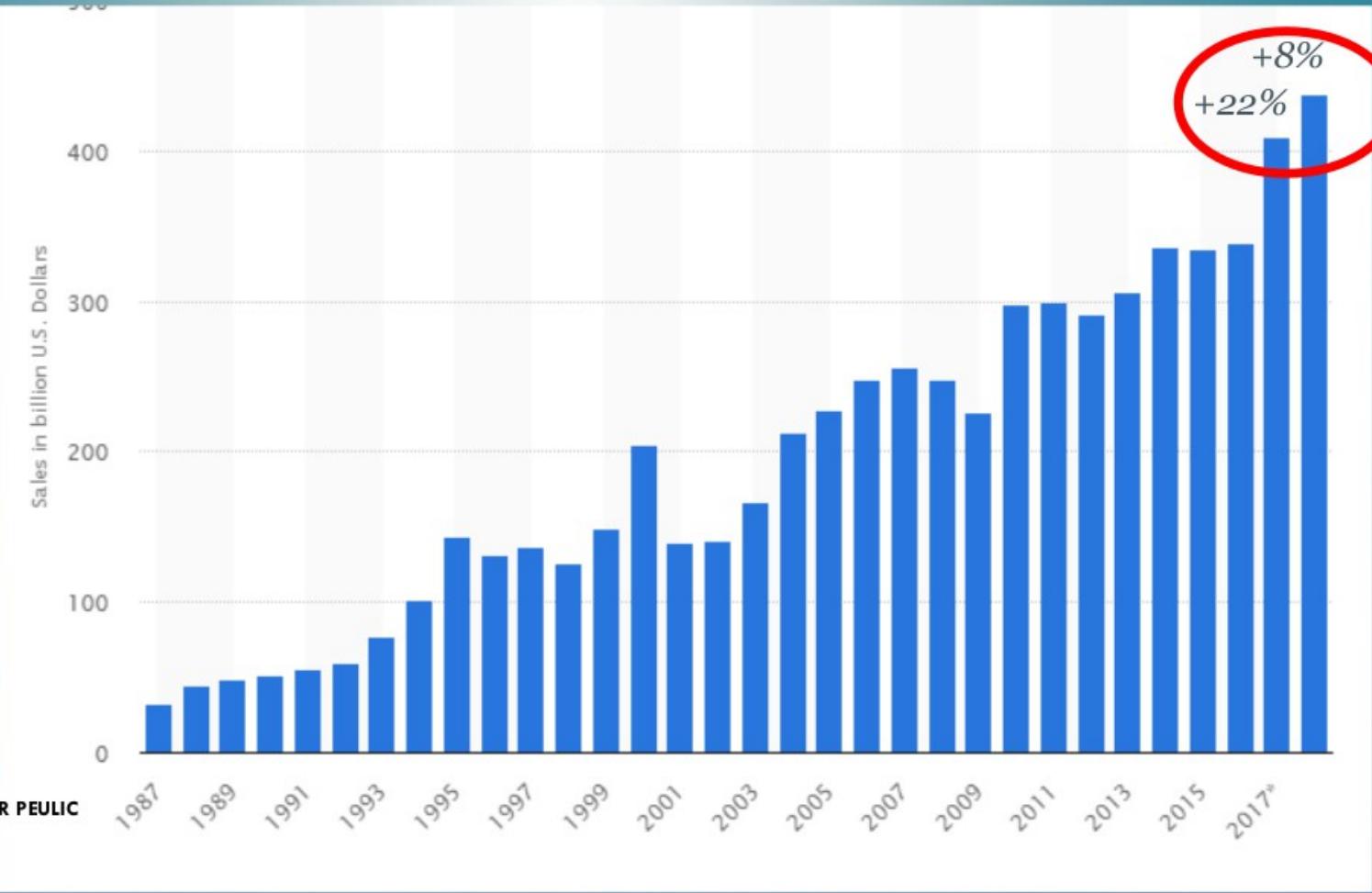


TREND RAZVOJA INDUSTRIJE POLUPROVODNIKA

GDE SE NALAZE ČIPOVI ?

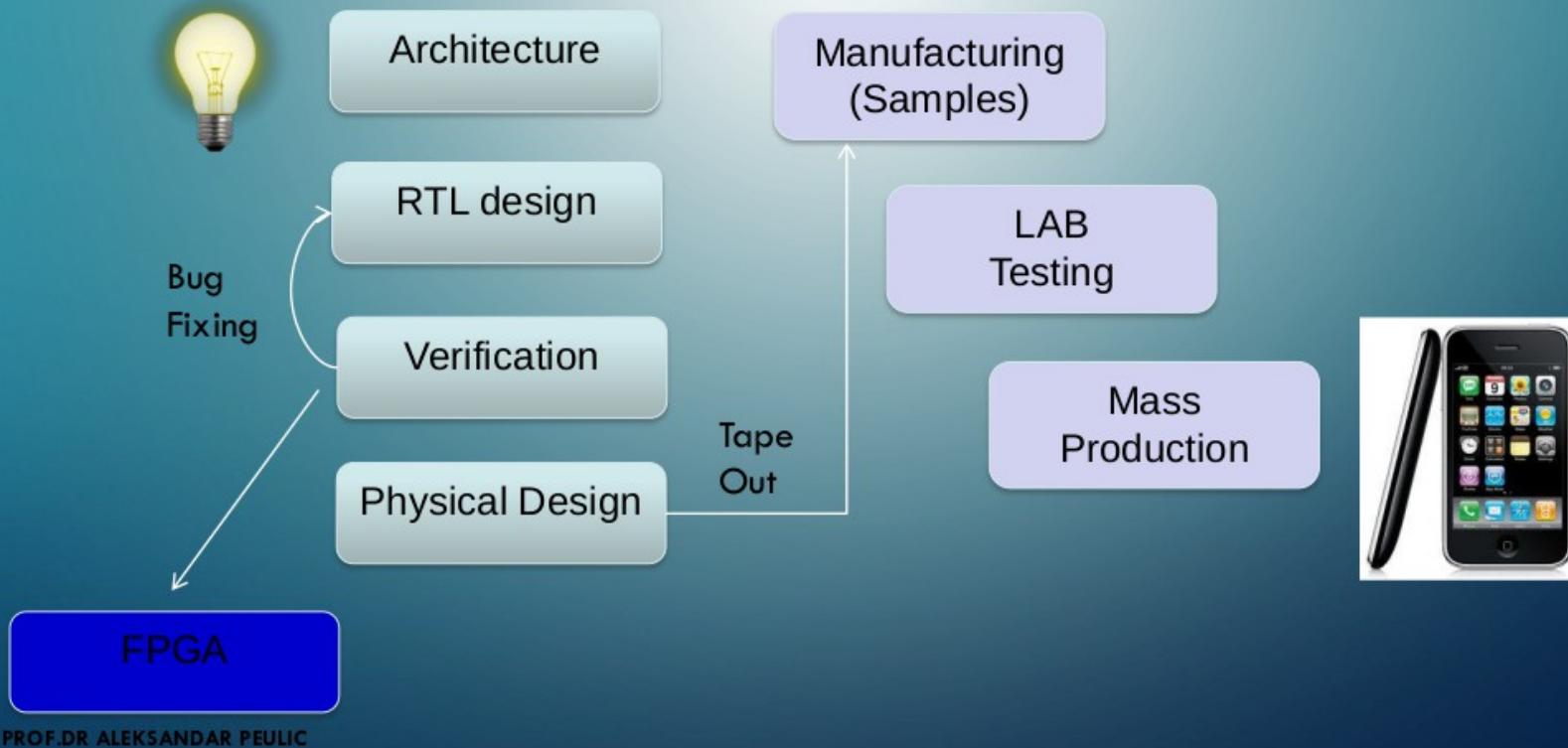


PRODAJA ČIPOVA U BILIONIMA USA DOLARA!

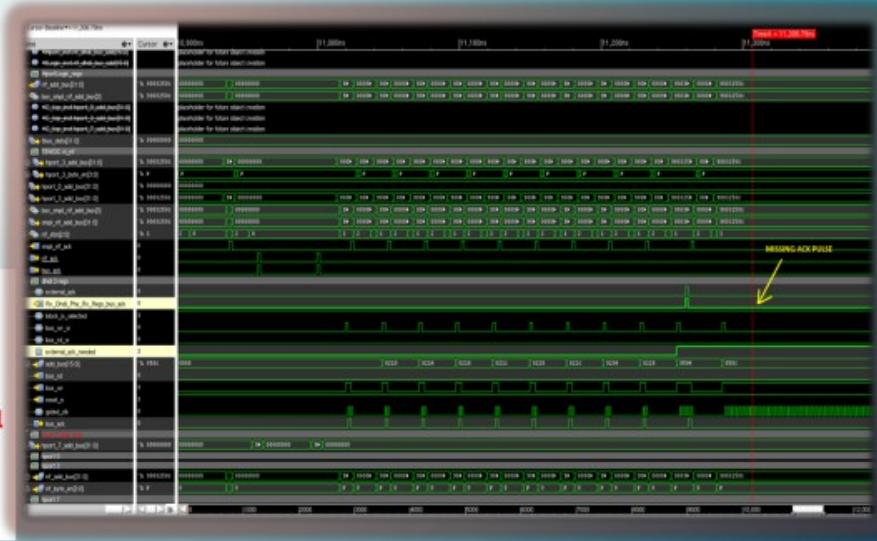
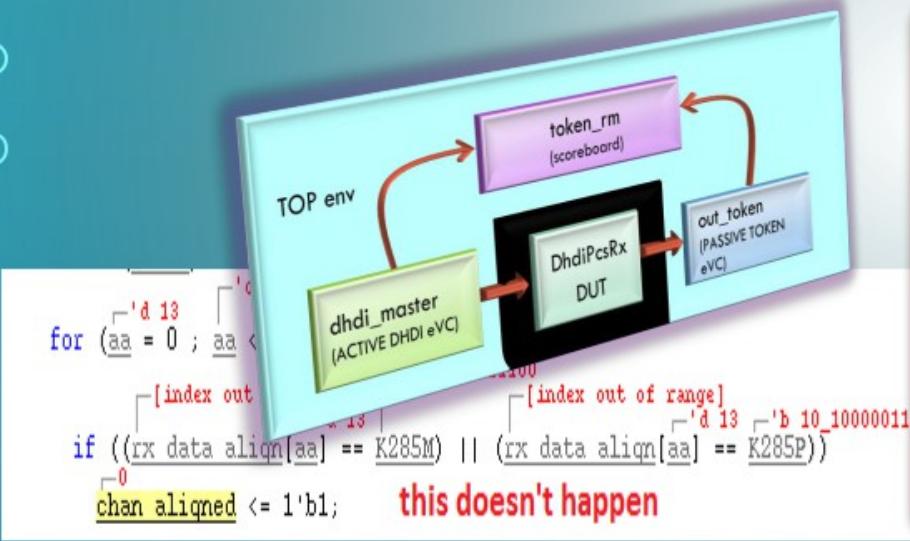


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Tok razvoja jednog chipa – Glavni koraci

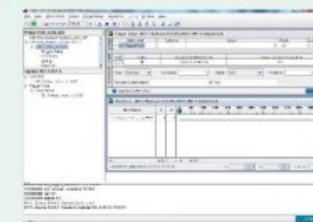
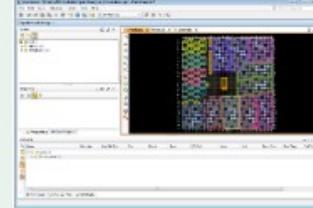
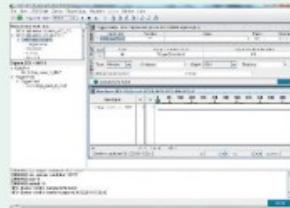
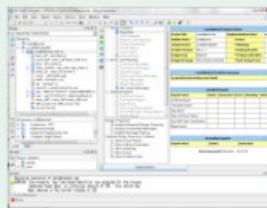


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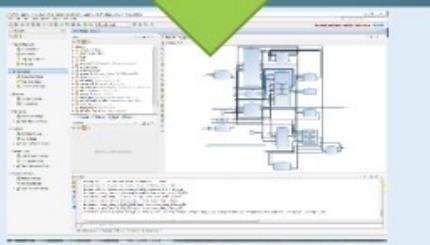


XILINX VIVADO DESIGN SUITE

ISE

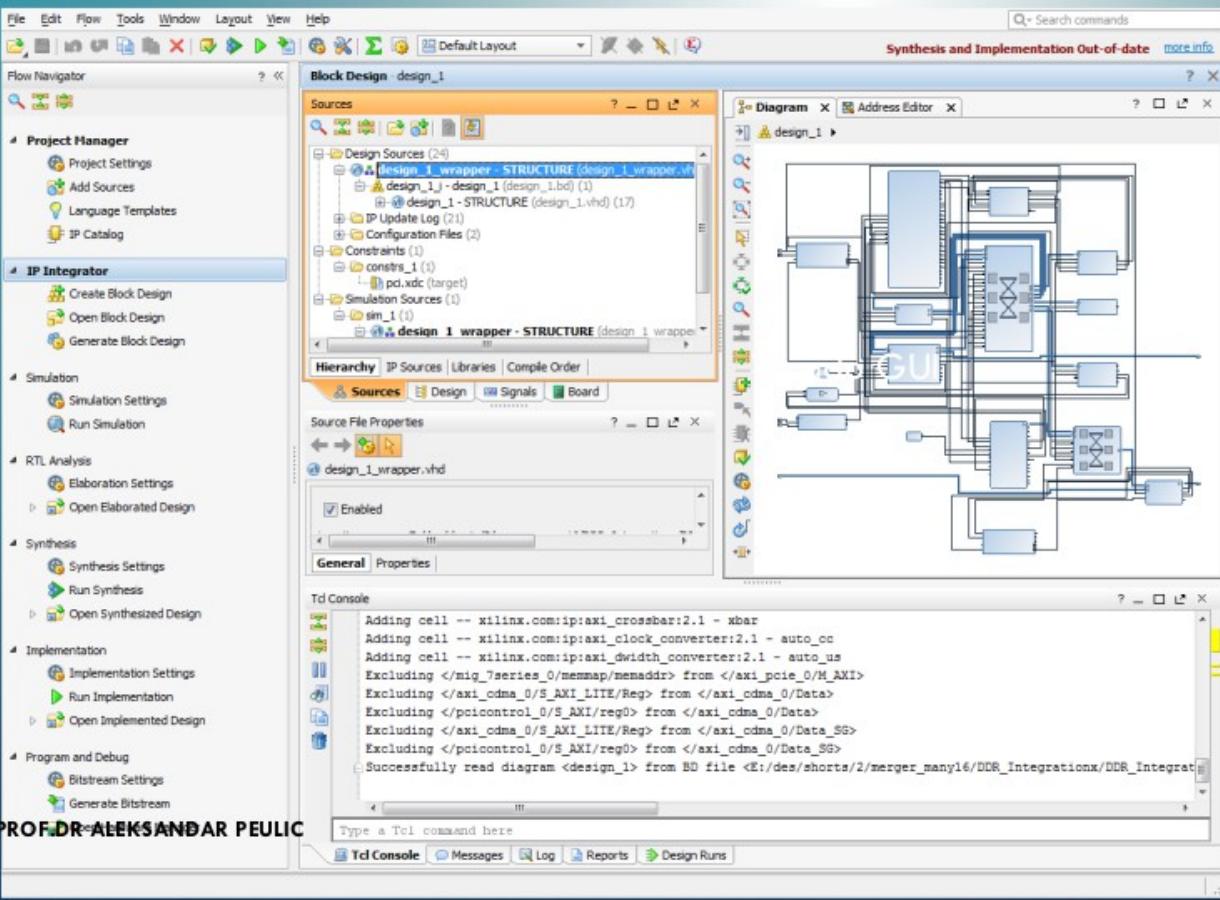


Vivado



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VIVADO GUI



HARDWARE:
RAZVOJNE PLOCE!!!!

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10

ORGANIZACIJA PREDMETA

- PREDAVANJA
- VEZBE

Kolokvijum verilog $2 \times 25 = 50$

Ispit VHDL 50 ili projekat 50

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12

ORGANIZACIJA PREDMETA

- PREDAVANJA
- VEZBE

Kolokvijum verilog $2 \times 25 = 50$

Ispit VHDL 50 ili projekat 50

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Razvojna ploca za vezbe PYNQ-Z2

